

TABLE S4 Switch error rates from computationally phasing hybrids simulated from derived inbred lines.

Simulated 'Hybrid'	Derived Lines Used as Priors	Population	Possible Switches	Switch errors	Rate
B11xB67	all	BSSS	12195	41	0.003
B17xB44	all	BSSS	12230	58	0.005
B39xB37	all	BSSS	11485	129	0.011
B43xB69	all	BSSS	12266	26	0.002
B73xB72	all	BSSS	12043	47	0.004
B78xB94	all	BSSS	11658	51	0.004
B89xB84	all	BSSS	11517	53	0.005
B11xB67	cycle 0	BSSS	12195	45	0.004
B17xB44	cycle 0	BSSS	12230	54	0.004
B39xB37	cycle 0	BSSS	11485	126	0.011
BB43xB69	cycle 0	BSSS	12266	32	0.003
B42xB54	all	BSCB1	11260	38	0.003
B90xB97	all	BSCB1	6499	52	0.008
B91xB95	all	BSCB1	7830	56	0.007
B99xB97	all	BSCB1	6779	52	0.008
BB42xB54	cycle 0	BSCB1	11260	57	0.005